IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Inventor: Joseph

Docket No.: TI-33028

Serial No.: 09/532,510

Group Art Unit: 2171

Filing Date: March 21, 2000

ΤI

Examiner: Le, U.T.

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NOV 1 2 2003

Title: STORAGE EFFICIENT MINIMIZATION LOGIC

Amendment Under 37 C. F. R. 111

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

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Jackie McBride

//-/2-63

Sir:

Responsive to the Examiner's Action of September 3, 2003, please amend this application as follows:

In the Claims:

Please amend the claims 16 as follows:

- 1 16. (Twice Amended) In an array of N data values, a method of
- determining an address for a result, the result being the output
- 3 of a binary operation defined in the array of data values each
- 4 data value having W bits, the method comprising the steps of:
- 5 (a) performing, at each computation stage i of log_2N ,
- 6 computation stages, $N/2^i$ binary operations on the data values of
- 7 $N/2^{i}$ pairs of input values using a binary operator, wherein each
- 8 input value includes a data value and a partial address, wherein
- 9 each of the binary operations generates a binary decision
- 10 representative of a selected data value and the partial address
- 11 of the selected data value:

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In re the Application of

Joseph

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FACSIMILE COVER SHEET

X FACSIMILE COVER SHEET NEW APPLICATION DECLARATION (# Pages) ASSIGNMENT (# Pages) FORMAL DRAWINGS INFORMAL DRAWINGS CONTINUATION APP'N (# Pages) DIVISIONAL APP'N		X AMENDMENT 111 EOT NOTICE OF APPEAL (I Page) APPEAL (# Pages) ISSUE FEE (# Pages) REPLY BRIEF (IN TRIPLICATE) (# Pages) ELECTION (1 page)
NAME OF INVENTOR(S):		RECEIPT DATE & SERIAL NO.:
Joseph		Serial No.: 09/532,510
TITLE OF INVENTION: Storage Efficient Minimization Logic		Filing Date: 03/21/2000
TI FILE NO.:	DEPOSIT ACCT. NO.:	1
TI-33028	20-0668	
FAXED: 11/12/2003 DUE: 12/03/2003 ATTY/SEC'Y: AMM/jsm		

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